# PE Lab\_ pre workshop Program

Dear all,

Thank you for registering to the PE\_lab pre-workshop.

Pre-workshop Program

10:00-10:05 Opening

10:05-10:20 Oral Session 1

10:20-10:35 Oral Session 2

10:35-10:50 Oral Session 3

10:50-11:05 Oral Session 4

11:05-11:10 Break

11:10-11:30 Short Presentation by students

11:30-12:10 Poster Session Meeting

12:10-12:15 Closing

Please Access the channel of oral session and poster session.

The each channel has program details and meeting details.

You will be able to contact the host (secretary) on the chat system of slack.

And you will be able to write Comments and questions for speakers and poster presenters on the chat system of slack.

Online meeting password will be sent by email.

### **Oral Session & Short Presentation**

DATE: May 29th, 2020 AM10:00-11:30



https://us02web.zoom.us/j/83881432375



Meeting password will be sent by email.

Please refrain from recording and photos.

Please kindly note that no materials will be distributed.

## Program.

10:00~10:05	Opening
10:05~10:20	Failure prediction of power devices based on real-time monitoring, Associate Prof. Akihiko Watanabe
10:20~10:35	Development of fast short-circuit protection system for advanced IGBT, Associate Prof. Masanori Tsukuda
10:35~10:50	Gate driving technology for multi-chip Insulated Gate Bipolar Transistors (IGBT) power devices, Dr. Tripathi Ravi Nath
10:50~11:05	Calculation of Single Event Burnout failure rate for high voltage devices under satellite orbit without fitting parameters,
	Prof. Ichiro Omura
11:05~11:10	Break

ABSTRACT: https://www.dropbox.com/sh/jyuu06q6tk3rzty/AABamCBolJvJ07iCN0k13uyka?dl=0



After this session, a poster session will be held

Please check the channel of poster session.

11:10 $\sim$ 11:30 Short Presentation by students

After the poster session, please return to the oral meeting room.

#### Poster session

DATE: May 29th, 2020 AM11:30-12:10

POSTER: https://www.dropbox.com/sh/cdv8b4evcqxueto/AABTvzJQAqTVMI79bdmw2d-ba?dl=0



Poster Room Meeting Password will sent by email.

You can freely enter and exit the conference room during the session.

[Poster Room A]



(zoom) https://us02web.zoom.us/j/83922906754



(poster01) 13kV UHV-IGBT : Feasibility Study, Atsushi Ito (poster02) Feasibility study of 650V rated FP-MOSFET, Kouki Hoshihara

[Poster Room B]



(zoom) https://us02web.zoom.us/j/88648602866

(poster04) Proposal of chip current nondestructive measurement method in power module, Narinao Nishio

[Poster Room C]



(zoom) https://us02web.zoom.us/j/83922906754

(poster05) Simultaneous Monitoring of Strain and Temperature for Power Semiconductor using Single IR camera, Yoshiki Masuda

(poster06) Verification of highly accurate power cycle test method, Yuma Kawauchi

After the poster session, please return to the oral meeting room.

### Thanks,

Contact:

pe omuralab@lofe.kyutexh.ac.jp

+81-93-695-6037